

PSO-Based PI Controller for Shunt APF in Distribution Network of Multi Harmonic Sources

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Abstract— PI controller tuning with the assumption of linear PWM model leads to unsatisfactory results under varying operating conditions. Optimal tuning of PI gains is required to get the best response of PI controllers. This paper presents a new method for harmonic suppression using particle swarm optimization (PSO) based PI controller (PSOPI) to estimate an efficient shunt active power filter (SAPF). The proposed filter is tested on a 13 bus industrial distribution system. The study system with the 18 study cases are simulated through MATLAB Simulink with different loading condition and single and multi harmonic sources. The results of applying the PIPSO controller are compared with those out of a double proportional controller (DPC). The obtained results ensure the effectiveness of the proposed filter and its superiority in reducing harmonics.

Index Terms— Active Power Filter, PSO Algorithm, PI Controller, Harmonics Suppression, Power Quality

I. Introduction

The evolution of the semiconductor switches technology; power electronics, converters, measurement strategies and control have been allowing a broader of the active power filter. One of the cornerstones of the active filter is its control scheme strategy that is implemented in the active filter controller.

There are myriad techniques for the control of the APF inverter. One of these techniques is the multi-loop feedback control, which was originally used in the single-phase uninterruptible power supply (UPS) application [1].

The reference signal of the multi-loop feedback control method could be any type of electric quantity such as voltage, current or power. The useful

information carried by the signal is its wave shape rather than the place where it comes from. Considerable efforts have been done in recent years to estimate the harmonic components [2-4].

Reference [2] proposes the use of four adaptive linear neurons (ADALINEs) networks for online extraction of the direct, inverse, and homopolar voltage components from a composite voltage. This neural network approach is based on a new voltage decomposition technique of unbalanced three phase systems.

An algorithm for harmonic estimation is presented in [3]. It utilizes a particle swarm optimizer with passive congregation (PSOPC) to estimate the harmonic components. Alongside a least-square (LS) method is used to estimate the amplitudes of these components. The PSOPC and LS method are executed alternately to minimize the error between the original signal and the signal reconstructed from the estimated parameters during the estimation process.

A neural network for extracting voltage and current waveforms is presented in [4]. It consists of an input layer with 32 nodes (32 samples), two hidden layers, and one-node output (1 sample) layer. The sampling rate is 960 HZ.

The ANN performs a sample by sample investigation of the input samples, the oldest sample is omitted and all remaining samples are displaced once to the neighbor position leaving an empty position to the new sample.

Artificial intelligence is one of the key areas to solve harmonic compensation in distribution system and make control more robust for transient conditions [5-8].

A fuzzy logic controlled shunt Active Power Filter (APF) for the elimination of current harmonics and reactive power compensation of nonlinear load is presented in [5]. The control scheme is based on indirect current control scheme in which only source

current is sensed for avoiding switching spikes. Three phase voltage and current signal is sensed using two voltage and current sensors..

The application of a novel neuro-fuzzy based control strategy which is used in order to improve the APF dynamics for wide range of variations of load current under various conditions is described in [6]. To improve dynamic behavior of a shunt active power filter and its robustness under range of load variations, instantaneous p-q theory is used with the inclusion of neural network filter for reference current generation and fuzzy logic controller for DC voltage control..

The implementation of Bacterial foraging optimization (BFO) to optimize the proportional-integral (PI) APF performance for load compensation under distorted supply voltage condition and sudden load fluctuation is proposed in [7].

Reference [8] uses the double proportional feedback control loop APF for uninterruptible power supply (UPS) application. The controller used as a single phase filter for a dedicated harmonic load and with a fixed voltage source.

A developed APF controller scheme based on a double proportional controller (DPC) feedback gains and a single-phase voltage-source half-wave bridge inverter is presented in [9]. In the outer loop, the generated current signal is fed back and compared with its reference. The resulting error signal is multiplied by the first proportional controller gain, and the output is added to the error signal obtained from comparing the inductor feedback voltage with its reference.

The resulting signal is multiplied by the second proportional controller gain in the inner control loop and the output is compared with a fixed switching frequency triangular waveform, which will be passed to the gate drive circuit. The proposed scheme is tested on a 13 bus industrial distribution system.

In this paper, a modification for the control circuit design of the APF proposed by [9] for compensation scheme is presented in this paper. This modification is summarized simply by removing the two proportional controllers from the inner and outer control loops for the voltage and current signals simultaneously and replaced by the PI controller in the inner loop. The feedback signals in this paper are the per-unit values of the voltage and current. The PI gains are estimated using particle swarm optimization (PSO) technique.

The scheme is applied on a 3-phase 13 bus industrial balanced distribution system. The measured voltage total harmonic distortion (THDv%) and current total harmonic distortion (THDi%) results are compared with those out of the DPC of [8]. The comparison shows the superiority of the proposed PSOPi controller over the DPC for compensating most of voltage and current harmonics at the chosen point of common coupling (PCC).

This paper is organized as follows: section II describes the proposed APF controller scheme, while section III shows the APF block diagram including the PSOPi controller. In section IV, the component of system under study will be illustrated. Section V shows the parameters of the filter and the 18 study cases that will be applied on the study system to evaluate its performance. Sections VI & VII will view the simulation results and analysis of the results respectively. Finally, conclusions out of the results will be shown in section VIII.

II. Proposed APF Scheme

The proposed topology of the active filter is consists of a single-phase voltage-source half-wave bridge inverter, a second-order low-pass filter with an inner inductor voltage feedback loop and an outer current feedback loop for current compensation as shown in Figure (1).

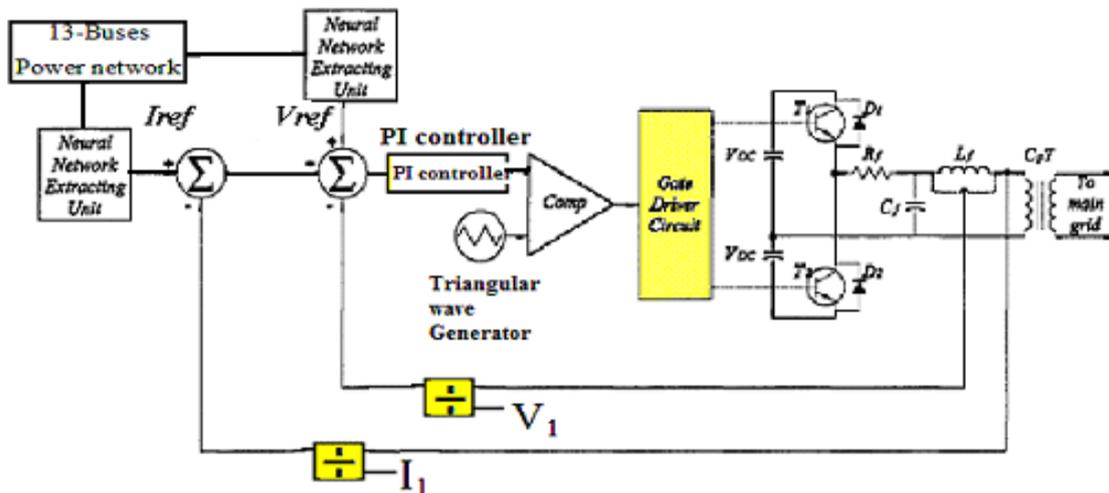


Fig. 1: Block diagram for current compensation scheme based on the inductor voltage with PI controller

In the outer loop, the generated current signal is fed back and divided by the fundamental current, then compared with its reference. The resulting error signal is added to the error signal obtained from comparing the inductor feedback voltage divided by its fundamental voltage with its reference.

The resulting signal is then processed by the PI controller, which obtained its gains by the PSO in the inner loop, and the output is compared with a fixed switching frequency triangular waveform, which will be passed to the gate drive circuit.

The gate drive circuit for current compensation will produce an optimum switched control signals for the Pulse Width Modulated (PWM) Voltage Source Inverter (VSI) of a number of CSC modules as shown in Fig. 1.

The neural network extracting units used in this study was developed and tested and its accuracy was assured in a previous work of the authors [4].

III. PSO-Based PI Active Power Filter

Particle Swarm Optimization (PSO) [10] has been an increasingly hot topic in the area of computational intelligence. PSO is yet another optimization algorithm that falls under the soft computing umbrella that covers genetic and evolutionary computing algorithms as well. As such, it lends itself as being applicable to a wide variety of optimization problems. One application that PSO has had tremendous success is in the training of Artificial Neural Networks (ANN), a fellow soft computing technique.

3.1 Particle Swarm Optimization Technique

Kennedy and Eberhart [11] introduce the PSO concept. PSO is basically developed through simulation of bird flocking in two-dimension space. The position of each particle is represented by XY axis position and the velocity is expressed by v_x and v_y (the particle velocities in the x direction and y direction respectively). Modification of the agent position is realized by the position and velocity information. This modification can be represented by the concept of velocity. Velocity of each agent can be modified by the following equations:

$$v_{i,n}^{k+1} = K[v_{i,n}^k + c_1 rand_1 * (pbest_{i,n} - x_{i,n}^k) + c_2 rand_2 * (gbest_n - x_{i,n}^k)] \tag{1}$$

$$K = \frac{2}{|2 - \phi - \sqrt{\phi^2 - 4\phi}|}, \tag{2}$$

and $\phi = c_1 + c_2, \phi > 4$

where,

K : constriction factor

$v_{i,n}^k$: velocity of agent i at iteration k and dimension n,

c_j : weighting factor,

$rand_j$: random number between 0 and 1,

$x_{i,n}^k$: current position of agent i at iteration k and dimension n,

$pbest_{i,n}$: pbest of agent i and dimension n,

$gbest_n$: gbest of the group at dimension n.

The current position (searching point in the solution space) can be modified by the following equation:

$$x_{i,n}^{k+1} = x_{i,n}^k + v_{i,n}^{k+1} \tag{3}$$

All velocities and positions are represented by vectors in an n dimensional space (number of space dimensions are the number of parameters to be optimized which are the three for the PID gains).

3.2 Simulated Control Circuit

Fig. 2 shows a block diagram for the simulated control circuit of the PI active power filter representing all components of Fig. 1.

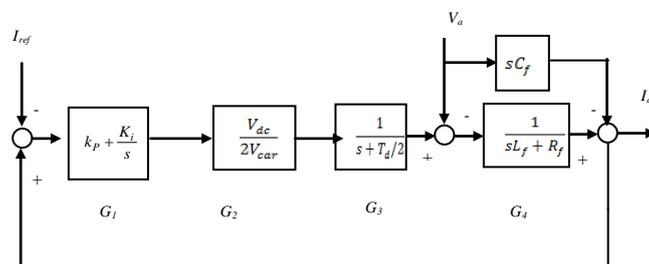


Fig. 2: Block diagram for the control circuit

where G_1 is the transfer function of the PI controller.

G_2 is the gain of the PWM converter

G_3 is the time delay caused by the PWM inverter

G_4 models the delay of the current of the active filter from the output voltage of the active filter.

k_p proportional gain

k_i integral gain

V_{dc} dc bus voltage of the inverter

V_{car} amplitude of carrier waveform

T_d PWM time delay

L_f filter inductance

R_f filter resistance

C_f filter capacitance

I_{ref} current harmonic component

I_{af} filter line output current waveform

V_a filter line output voltage waveform

V_a can be considered as disturbance, and I_{ref} denotes the harmonic content of the load current. The transfer function of the block diagram of Figure (2) is as follow:

$$G(s) = \frac{V_{dc}(k_p s + k_i)}{2V_{car}L_f s^2 + (2V_{car}R_f + V_{dc}k_p)s + k_i} \quad (4)$$

3.3 Objective Function

To increase the system damping, the eigenvalue-based objective function is considered as follows:

$$J = \max \left\{ \begin{array}{l} \text{Real}(\lambda_i) \\ \lambda_i \in \text{electromechanical modes} \end{array} \right\} \quad (5)$$

where $\text{Real}(\lambda_i)$ is the real part of the i^{th} electromechanical mode eigenvalue of the A matrix. In the optimization process, it is aimed to minimize J in order to shift the poorly damped eigenvalues to the left in s-plane.

The proposed approach employs PSO to solve this optimization problem and search for an optimal set of PI gains. MATLAB files for the PSO have been developed in [12]. It is modified and employed to get the optimal values of the PI gains. In addition to the main PSO program, additional programs are designed to get the state-space representation of the system.

IV. Study System

The proposed scheme is applied on a medium-sized 13 bus Balanced Industrial Distribution test system of an industrial plant [13]. A single-line diagram of this system is shown in Fig. 3. The plant is fed from a utility supply at 69 kV at bus B100 and the local plant distribution system operates at 13.8 kV. Due to the balanced nature of this system, only positive sequence data is provided. The assumptions used to conduct a harmonic analysis of the example industrial system include the following [14]:

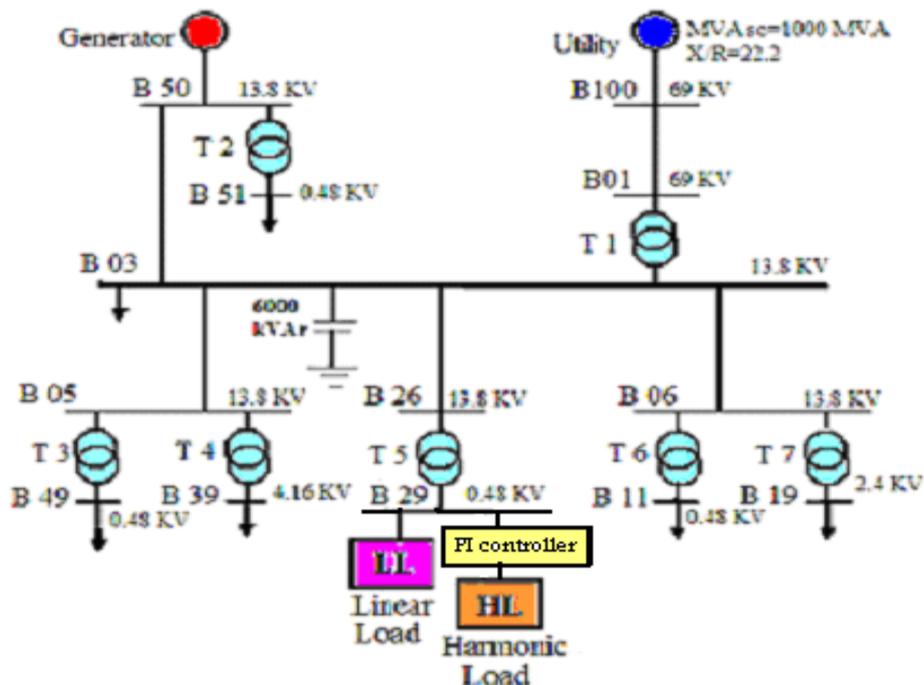


Fig. 3: Single line diagram of 13-Bus Industrial Distribution System

- Capacitance of the short overhead line and all cables are neglected.
- System equivalent impedance is determined from the fault MVA and X/R ratio at the utility connection point. These values are 1000MVA and 22.2 p.u. respectively.
- The local generator is represented as a simple Thevenin equivalent. The internal voltage, determined from the converged power flow solution, is $13.98\angle -1.52^\circ$ kV. The equivalent impedance is the sub-transient impedance, which is $(0.0366 + j1.3651)\Omega$.
- The plant power factor correction capacitors are rated at 6000 kVAR. As is typically done, leakage and series resistance of the bank are neglected in this study.
- All loads are modeled as series RL circuits. This approach is taken instead of parallel RL modeling to get more accurate representation of induction motors without extremely detailed motor models.
- Frequency dependence of model resistance is neglected.
- Transformer magnetizing branch effects are neglected.

- Transformer winding losses increase as a function of frequency is also neglected.
- The generator shown in Fig. (3) is 60Hz diesel type.

The source of harmonics is the Adjustable Speed Drive (ASD) connected to bus 29.

This test system is simulated in [15] using the MATLAB-Simulink program as shown in Fig. 4 and the PCC is chosen to be at the secondary side of the transformer T5 (bus 29). The voltage and current waveforms at this bus are monitored and the THD is measured to protect the linear load (LL) connected to bus 29 from harmonics generated by the harmonic generating load (HL) connected to the same bus and also to reduce the harmonic distortion at all buses of the system according to the limits specified by the IEEE 519-1992 [13]. The harmonic generating load connected to bus 49 in the original system is replaced here by an equivalent linear load. It was found that the harmonic source at bus 49 has a very small effect on the voltage and current waveform at bus 29.

This small effect because Bus 29 is not on the direction of the current harmonics flow from the supply to bus 49.

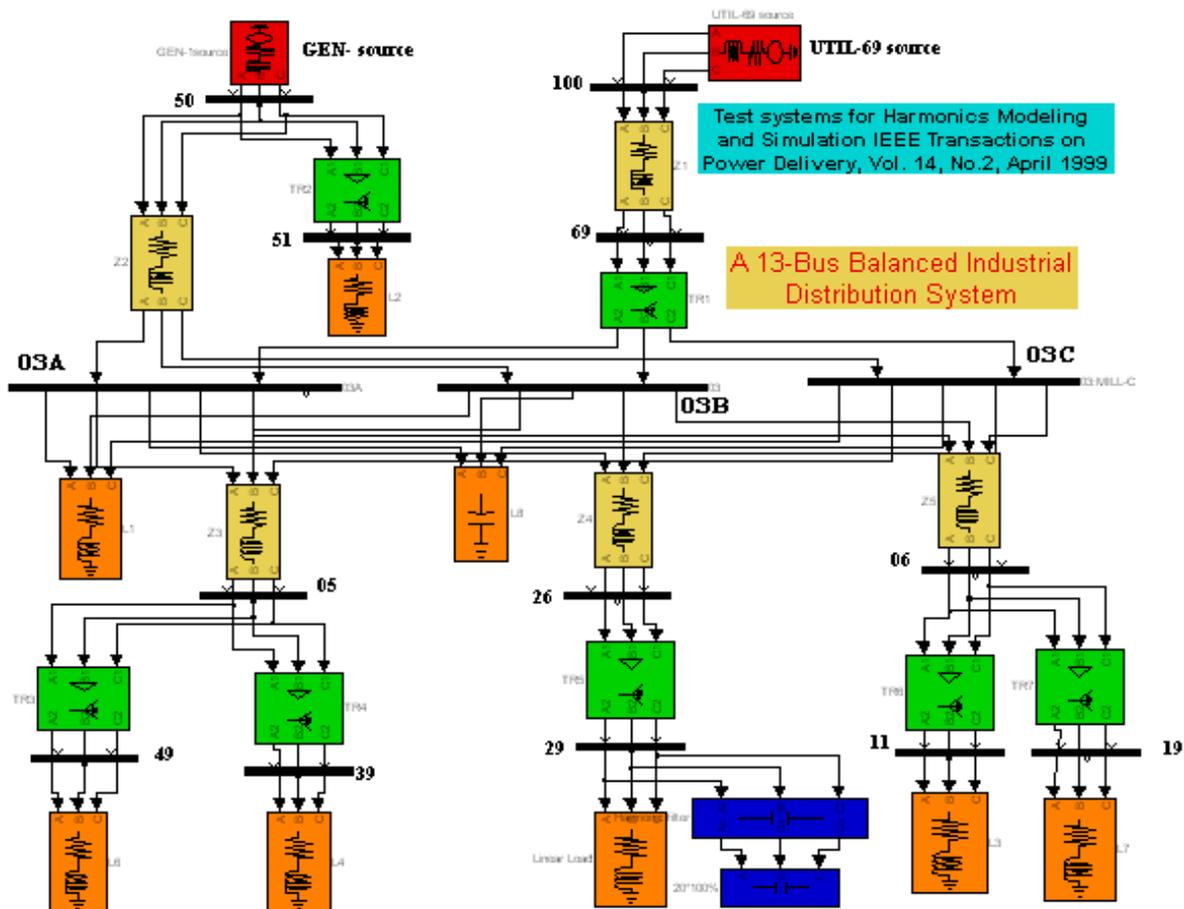


Fig. 4: Simulation of the test system on the Matlab program

The ASD used as a harmonic source consists of set of 22 kW three phase induction motors connected in parallel and each of them is served by 22 kW pulse width modulated (PWM) inverter. The ASD is modeled by a harmonic current source. Each harmonic component is modeled by a current source with a frequency multiple integer of the fundamental frequency. The magnitude and phase angle of the harmonic component are related to the fundamental current [16].

V. Performance Evaluation of the Proposed PSOPI Controller

The Multi-loop feedback controller parameters presented here in table (1) have been used with the particle swarm optimization technique for the estimation of the PI-controller gains.

The obtained values of the PI gains using PSO technique are:

$$k_p = 0.843, k_i = 176.$$

The obtained gains used with the new proposed PI-controller filter replacing the two proportional controls presented in [last paper] and also using the per unit

values of the reference voltage and currents instead of the physical quantities in the reference and feedback loops.

Table 1: Parameter Of A Current Compensation Filter Equipped With A ML_PSOPI Controller

| | |
|-------------------------------|---------------|
| AC Source Voltage, V_s | 480 V |
| DC Inverter Voltage, V_{DC} | 220 V |
| Fundamental Frequency | 60 Hz |
| Sampling Frequency | 10,000 Hz |
| PWM Carrier Frequency | 5,000 Hz |
| Line Resistance, R_s | 0.1 Ω |
| Line Inductance, L_s | 1 mH |
| Filter Resistance, R_c | 0.1 Ω |
| Filter Inductance, L_c | 0.1 mH |
| Filter Capacitance, C_c | 4,500 μF |

5.1 Study Cases

18 different combinations of harmonic load locations and loading levels are chosen to extensively cover the study of the harmonic problem for the system under study. These combinations are shown in table (2).

Table 2: Study Cases for Different Load Combinations

| Case | Bus 29 | | Bus 51 | | Bus 11 | | Bus 29 LL | |
|------|----------------------|------------------|----------------------|------------------|----------------------|------------------|-----------|-------|
| | Total motor load, kW | ASD load level % | Total motor load, kW | ASD load level % | Total motor load, kW | ASD load level % | kW | kVA r |
| 1 | 800 | 75 | - | - | 1200 | 50 | 700 | 200 |
| 2 | 800 | 75 | 400 | 100 | 800 | 100 | 600 | 400 |
| 3 | 800 | 50 | 800 | 75 | 800 | 100 | 600 | 600 |
| 4 | 800, 600 | 50, 100 | - | - | - | - | 200 | 200 |
| 5 | 800, 400 | 75, 50 | - | - | - | - | 600 | 200 |
| 6 | 800, 400, 400 | 50, 100, 75 | - | - | - | - | 300 | 0 |
| 7 | 600 | 100 | 600 | 100 | 800 | 100 | 600 | 600 |
| 8 | 800, 400 | 100, 75 | - | - | - | - | 300 | 100 |
| 9 | 400, 400, 400 | 100, 75, 50 | - | - | - | - | 400 | 200 |
| 10 | 1200 | 75 | 800 | 50 | - | - | 400 | 100 |
| 11 | 400, 800 | 100, 50 | 800 | 50 | - | - | 500 | 200 |
| 12 | 1200 | 50 | - | - | 800 | 50 | 700 | 300 |
| 13 | 400, 1200 | 75, 50 | - | - | 800 | 50 | 500 | 100 |
| 14 | 400 | 50 | 800 | 75 | 800 | 50 | 1200 | 300 |
| 15 | - | - | 800 | 75 | 1200 | 50 | 1000 | 600 |
| 16 | 1200 | 100 | - | - | - | - | 200 | 100 |
| 17 | 400 | 75 | 400 | 75 | 400 | 75 | 800 | 800 |
| 18 | 800 | 100 | 1200 | 50 | 1200 | 50 | 300 | 300 |

In the study case # 1, for example, motor loads of 800 kW and 1200 kW are connected, respectively, to buses 29 and 11 and a linear load of (700 kW+ 200 kVA_r) is also connected to bus 29. The ASDs supplying the 800 kW and 1200 kW motor loads are run, respectively, at 75% and 50% of their nominal capacities.

The study cases in table (1) are chosen to get the maximum possibility for harmonic current circulation. The cases could be divided according to the number, location and loading level of harmonic loads into three main categories as follows:

1- A single ASD, with different ASD and motor loading levels, is connected to some different system buses, one at a time, in the study cases # 4, 5, 6, 8, 9 and 16.

2- Two ASDs are simultaneously connected to two different buses in the study cases # 1, 10, 11, 12 and 13.

3- Three ASDs are Simultaneously connected to three different busses in the study cases # 2, 3, 7, 14, 17, and 18.

5.2 Applying the ML-ANN on the Test System

In this stage, two independent ML-ANNs with shift method are used to estimate the fundamental frequency voltage and current components for the industrial distribution system.

The estimated fundamental voltage and current waveforms are subtracted, respectively, from the polluted voltage and current waveforms as shown in fig. 5 and fig. 6. The resulting voltage and current harmonics are used as reference signals for the PSOPi controller in the next stage.

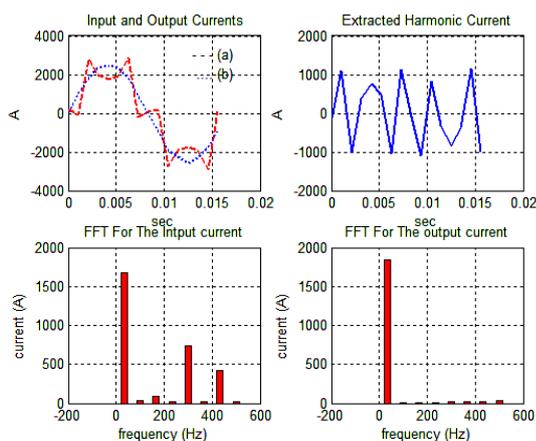


Fig. 5: Current wave forms and Fast Fourier Transform

Fig. 5 shows the distorted input current, the estimated fundamental current wave form and the extracted harmonic contents from the input at bus 29 for case 6. Also present the Fast Fourier Transform (FFT) decomposition for the distorted input and the estimated output signal at bus 29 for case 6.

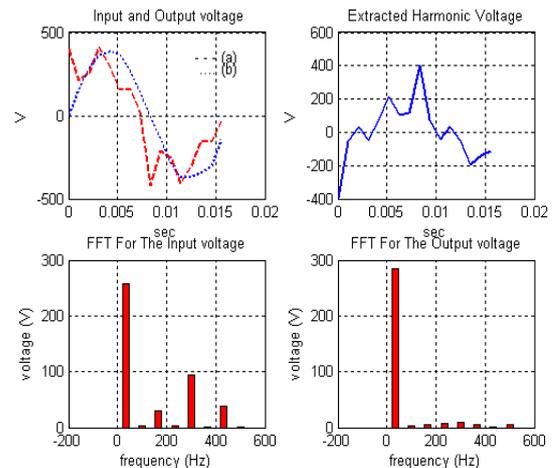


Fig. 6: Voltage wave forms and Fast Fourier Transform

Fig. 6 shows the distorted input voltage, the estimated fundamental voltage wave form and the extracted harmonic contents from the input at bus 29 for case 6. Also present the Fast Fourier Transform (FFT) decomposition for the distorted input and the estimated output signal at bus 29 for case 6.

VI. Simulation Results

The performance of the proposed PSOPi control scheme is examined for harmonics control application throughout the 13-bus industrial distribution test system described in details in section IV. The control scheme is tested for 12 cases (cases 7 to 18) since the first 6 cases are used for training the multilayer neural network.

The voltage and current waveforms are monitored at each case, and the current total harmonic distortions at buses 51, 29 and 11 are measured when using the multi-loop feedback control scheme with PI-controller based on PSO technique.

6.1 Detection of Voltage THD at All Buses

The voltage total harmonic distortion at all the system buses is measured to indicate the effect of the proposed PSO-based filter. Tables (3 & 4) present the THD_v% at all system buses for the different cases after the connection of the ML-PI/PSO filter at bus 29.

Looking at the obtained results, it is found that the THD_v% at bus 29 is varying from case to case according to harmonic source rating and location in the system. The THD_v% reaches in some cases 8.837% as in case 18, which could be seen as the worst case from point of view of the THD_v% at bus 29. While in other case the THD_v% may reach a very small value (0.952%) as in case 16.

Table 3: THDv% for the Study Cases #7 to #12 with the PIC Filter

| Bus | THDv % | | | | | |
|-----|--------|--------|--------|---------|---------|---------|
| | Case 7 | Case 8 | Case 9 | Case 10 | Case 11 | Case 12 |
| 100 | 0.724 | 0.278 | 0.449 | 0.560 | 0.575 | 0.506 |
| 69 | 1.297 | 0.285 | 0.450 | 0.698 | 0.714 | 0.672 |
| 03 | 6.728 | 0.443 | 0.526 | 2.659 | 2.690 | 2.834 |
| 50 | 6.517 | 0.435 | 0.521 | 2.567 | 2.597 | 2.747 |
| 51 | 11.430 | 0.396 | 0.477 | 8.811 | 8.821 | 2.678 |
| 05 | 6.727 | 0.443 | 0.526 | 2.658 | 2.690 | 2.834 |
| 49 | 5.630 | 0.371 | 0.513 | 2.236 | 2.263 | 2.376 |
| 39 | 6.307 | 0.389 | 0.520 | 2.505 | 2.534 | 2.665 |
| 26 | 6.729 | 0.444 | 0.526 | 2.659 | 2.690 | 2.834 |
| 06 | 6.730 | 0.443 | 0.526 | 2.659 | 2.690 | 2.830 |
| 11 | 13.830 | 0.403 | 0.481 | 2.763 | 2.804 | 8.581 |
| 19 | 6.348 | 0.390 | 0.521 | 2.520 | 2.549 | 2.677 |
| 29 | 6.594 | 0.970 | 0.830 | 2.758 | 2.736 | 2.776 |

Table 4: THDv% for the Study Cases #13 to #18 with the PIC Filter

| Bus | THDv % | | | | | |
|-----|---------|---------|---------|---------|---------|---------|
| | Case 13 | Case 14 | Case 15 | Case 16 | Case 17 | Case 18 |
| 100 | 0.448 | 0.722 | 0.828 | 0.341 | 0.609 | 0.891 |
| 69 | 0.625 | 1.299 | 1.592 | 0.343 | 0.870 | 1.600 |
| 03 | 2.760 | 6.735 | 8.468 | 0.483 | 3.905 | 8.311 |
| 50 | 2.674 | 6.511 | 8.189 | 0.476 | 3.778 | 8.032 |
| 51 | 2.613 | 12.500 | 13.210 | 0.466 | 6.483 | 14.540 |
| 05 | 2.760 | 6.734 | 8.467 | 0.483 | 3.905 | 8.310 |
| 49 | 2.315 | 5.589 | 7.031 | 0.449 | 3.262 | 6.923 |
| 39 | 2.599 | 6.288 | 7.910 | 0.460 | 3.658 | 7.788 |
| 26 | 2.760 | 6.734 | 8.468 | 0.484 | 3.905 | 8.313 |
| 06 | 2.756 | 6.732 | 8.463 | 0.483 | 3.903 | 8.306 |
| 11 | 8.560 | 10.060 | 14.320 | 0.472 | 6.374 | 14.280 |
| 19 | 2.611 | 6.323 | 7.954 | 0.461 | 3.558 | 7.831 |
| 29 | 2.831 | 5.505 | 8.200 | 0.952 | 3.652 | 8.837 |

6.2 Detection of Current THD in Lines Feeding Buses 51, 11 & 29

Tables 5 and 6 present the THDi% found in currents fed to buses 51, 11 and 29 for the 12 study cases after connecting the PSO-based PI filter at bus 29. The

THDi% of harmonic currents circulated in the LL at bus 29 is also given in the same tables. It is found that the THDi% is varying from case to another, according to harmonic source rating, and from one bus to another, according to the location of the bus in the system.

Table 5: THDi% in Currents Fed to Buses 51, 11 and 29 with PIC Filter for the Study Cases #7 to #12

| Buses | THDi % | | | | | |
|-------|--------|--------|--------|---------|---------|---------|
| | Case 7 | Case 8 | Case 9 | Case 10 | Case 11 | Case 12 |
| 51 | 28.410 | 0.286 | 0.402 | 30.250 | 30.250 | 0.766 |
| 11 | 42.160 | 0.287 | 0.403 | 0.745 | 0.788 | 41.590 |
| 29 | 2.103 | 2.755 | 2.680 | 3.110 | 2.498 | 2.153 |
| 29:LL | 1.571 | 0.571 | 0.524 | 1.497 | 1.144 | 1.077 |

Table 6: THDi% in Currents Fed to Buses 51, 11 and 29 with PIC Filter for the Study Cases #13 to #18

| Buses | THDi % | | | | | |
|-------|---------|---------|---------|---------|---------|---------|
| | Case 13 | Case 14 | Case 15 | Case 16 | Case 17 | Case 18 |
| 51 | 0.724 | 34.940 | 34.920 | 0.342 | 22.080 | 38.070 |
| 11 | 41.590 | 41.550 | 51.050 | 0.343 | 31.400 | 51.110 |
| 29 | 2.520 | 2.597 | 2.170 | 3.286 | 1.318 | 3.700 |
| 29:LL | 1.710 | 3.048 | 2.820 | 0.517 | 0.962 | 1.989 |

6.3 Comparison of the PSO-based PI Controller and the Two Proportional Controller

Cases #16 and #18 are selected to compare the effectiveness of the two APF controller schemes on harmonics on the 13 bus industrial distribution system. In each case the voltage and current waveforms at bus 29 and the current drawn by the linear load bus 29:LL are monitored. Also at each case the THDv% at all system buses with the PSOPI filter and with the DPC filter [9] are compared. The same comparison is done for THDi% at bus 51, 11, 29 and bus 29:LL.

Case 16

Fig. 7 presents the simulation voltage and current waveforms for the multi-loop feedback control system with the PSOPI filter at bus 29 when applied to the test system for case 16.

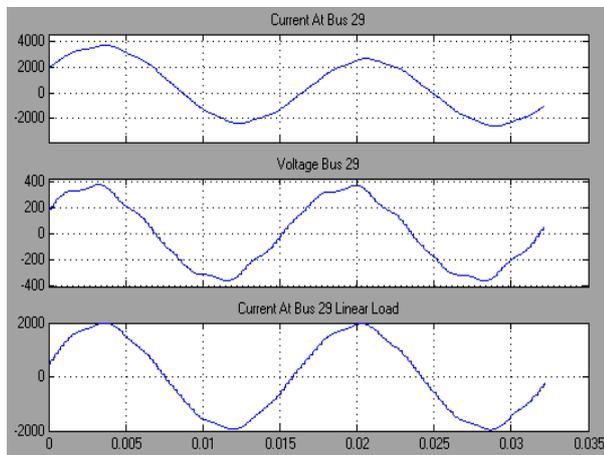


Fig. 7: Voltage and current waveforms at Bus 29 for case 14 with PIC filter

Fig. 8 presents the measured THDv% at all system buses with the PSOPI filter and with the DPC also Fig. 9 presents the measured THDi% at bus 51, 11, 29 and 29:LL with the PSOPI filter and without the connection of the filter when applied to the test system case 16.

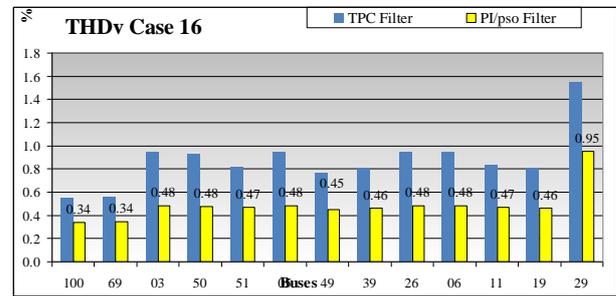


Fig. 8: THDv% at all system buses for case 16 with PIC and without filter

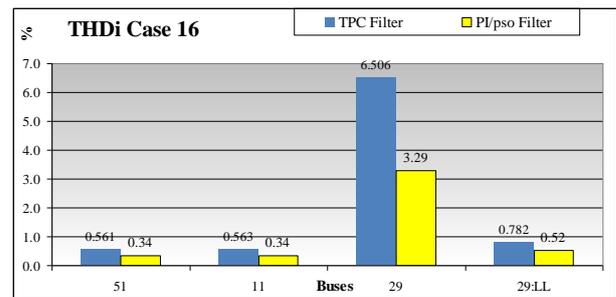


Fig. 9: THDi% for case 14 with PIC and without filter

Case 18

Fig. 10 presents the simulation voltage and current waveforms for the multi-loop feedback control system with PSOPI at bus 29 when applied to the test system case 18.

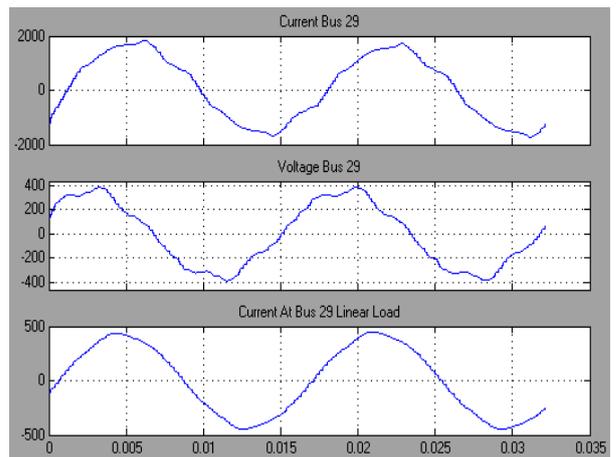


Fig. 10: Voltage and current waveforms at Bus 29 for case 18 with PIC filter

Fig. 11 presents the measured THDv% at all system buses with the PSOPi filter and with the DPC filter. Fig. 12 also presents the measured THDi% at bus 51, 11, 29

and 29:LL with the PSOPi filter and with the DPC filter when applied to the test system case 18.

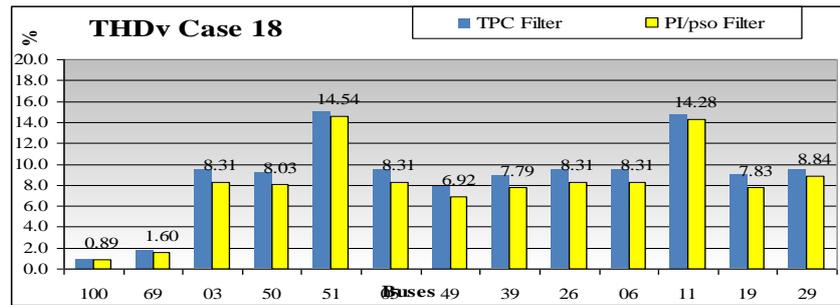


Fig. 11: THDv% at all system buses for case 18 With PIC and without filter

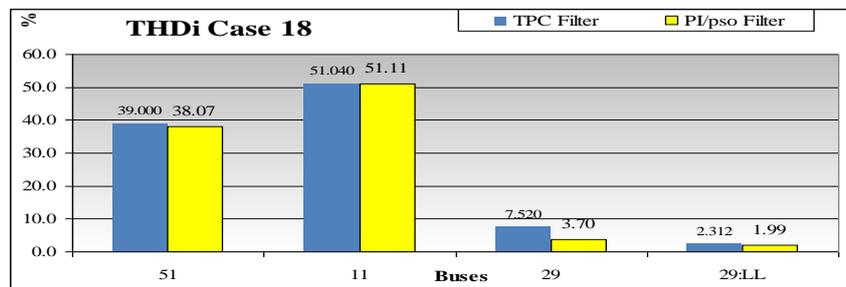


Fig. 12: THDi% for case 18 with PIC and without filter

Tables (7 & 8) presenting the percentage improvement in THDv% and THDi% respectively, caused by using the POSPI controller compared with

that of using the DPC for the 13 bus industrial distribution system.

Table 7: % improvement of THDv when using a ML-PI controller as compared with that of a ML-PP controller

| Percentage improvement in THDv % in study cases | | | | | | | | | | | | | |
|---|------|------|------|------|------|------|------|------|------|------|------|------|------|
| Buses | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 | 16 | 17 | 18 | Av% |
| 100 | 18.6 | 28.9 | 15.2 | 3.4 | 3.5 | 14.7 | 15.9 | 4.5 | 3.6 | 33.5 | -3.6 | -3.7 | 11.2 |
| 69 | 8.7 | 26.9 | 15.5 | 3.4 | 3.3 | 10.0 | 9.7 | 3.9 | 1.1 | 33.4 | -1.9 | 1.0 | 9.6 |
| 03 | 3.1 | 12.2 | 14.4 | 3.1 | -0.1 | 2.2 | 1.7 | 3.6 | 0.1 | 24.3 | -0.2 | 2.7 | 5.6 |
| 50 | 3.1 | 12.9 | 14.5 | 3.1 | 6.6 | 2.2 | 1.8 | 3.6 | 0.1 | 24.6 | -0.2 | 2.7 | 6.2 |
| 51 | 0.0 | 31.2 | 23.2 | 0.0 | 1.4 | 4.3 | 4.0 | 0.0 | -0.2 | 23.8 | -0.1 | -0.1 | 7.3 |
| 05 | 3.1 | 12.2 | 14.4 | 3.1 | 3.3 | 2.2 | 1.7 | 3.6 | 0.1 | 24.3 | -0.2 | 2.7 | 5.9 |
| 49 | 3.2 | 24.3 | 15.2 | 3.2 | 3.4 | 2.3 | 2.1 | 3.6 | 0.1 | 24.5 | -0.3 | 2.8 | 7.0 |
| 39 | 3.1 | 22.1 | 15.3 | 3.2 | 3.4 | 2.2 | 1.9 | 3.6 | 0.1 | 24.2 | -0.2 | 2.8 | 6.8 |
| 26 | 3.1 | 12.1 | 14.4 | 3.1 | 3.3 | 2.2 | 1.7 | 3.6 | 0.1 | 24.2 | -0.2 | 2.7 | 5.9 |
| 06 | 3.1 | 12.2 | 14.4 | 3.1 | 3.3 | 2.2 | 1.7 | 3.6 | 0.1 | 24.3 | -0.2 | 2.7 | 5.9 |
| 11 | -0.1 | 20.4 | 23.1 | -0.2 | -0.4 | 0.1 | -0.2 | 0.0 | -0.1 | 23.6 | -0.2 | 0.4 | 5.5 |
| 19 | 3.1 | 22.0 | 15.3 | 3.2 | 3.4 | 2.2 | 1.9 | 3.6 | 0.1 | 24.2 | 3.1 | 2.8 | 7.1 |
| 29 | 4.3 | 16.6 | 8.9 | 0.3 | 1.7 | 2.9 | -0.5 | 12.8 | -0.8 | 14.9 | 5.2 | 0.4 | 5.6 |

Table 8: % improvement of THDi when using a ML-PI controller as compared with that of a ML-PP controller

| Percentage improvement in THDi % in study cases | | | | | | | | | | | | | |
|---|------|------|-----|------|------|------|------|------|-------|------|------|------|------|
| Buses | 7 | 8 | 9 | 10 | 11 | 12 | 13 | 14 | 15 | 16 | 17 | 18 | Av% |
| 29 | 15.5 | 7.9 | 4.6 | 11.9 | 22.2 | 11.4 | 14.0 | 8.9 | 0.5 | 2.2 | 15.0 | 50.2 | 13.7 |
| 29:LL | 8.9 | 24.3 | 0.0 | 1.6 | 2.1 | 7.0 | 2.1 | 12.9 | -12.0 | 21.8 | 2.1 | 2.2 | 6.1 |

VII. Analysis of the Results

The results obtained for the PSOPI controller in tables (3 & 4) for the THDv% are compared with those obtained under the DPC [9] at bus 29 and with 5% allowable limit set by the IEEE 519-1992 [13], and the comparison are shown in table (7). The following comments are reached:

- Applying PSOPI controller improves 92.4% of THDv% of the measured cases at all the system buses, while it improves all the average THDv% at system buses.
- Applying the PSOPI filter improves and decrease the THDv% at bus 29 in all cases except cases 13 & 15.
- The THDv% at bus 29, which is directly connected to the proposed PSOPI filter reaches 8.83 in the study case #18 (the highest THDv% in the measured 12 study cases) while it was 9.473 with the TPC filter and 17.77 without filter.
- The THDv% exceeds the allowable 5% limit by connecting PSOPI to bus 29 for 30.1% of the measurements, while it exceeds by 32.05% with DPC.

The results obtained for the PSOPI controller in tables (5 & 6) for the THDv% are compared with those obtained under the DPC at bus 29 and with 5% allowable limit set by the IEEE 519-1992 for low voltage systems in which the short-circuit current lies between 20: 50 times the load currents, and the comparison are shown in table (8). The following comments are reached:

- The THDi% of the linear load (bus 29:LL), which is connected in parallel with the harmonic load, with PSOPI controller filter is less than the allowable limit in all the 12 study cases, while without filter it exceeds the limit in 7 cases.
- Current total harmonic distortion (THDi%) for bus 29 & bus 29:LL are improved due to the connection of the PSOPI control scheme than with DPC in all cases except with case #15 at bus 29:LL.
- The THDi% at bus 29 with PSOPI filter do not exceed the 5% allowable limit in all cases, while with DPC filter exceed the limit in five cases (cases #8, #10, #13, #16, and #18).
- Average percentage improvement for the PSOPI filter over the DPC filter for the THDi% at bus 29 reaches 13.7% while for the THDi% at linear load connected to bus 29:LL reaches 6.1%.
- The THDi% at bus 29:LL with PSOPI filter reaches 3.048% and 2.82% in the study cases #14 and #15 respectively, which are the worst two cases and less than 2% in the other 10 cases.

- The THDi% reaches 3.7% at bus 29 in case #18, which could be seen as the worst case at this bus. However, it should be noticed that the THDi% at this bus was 7.52% before adding the PSOPI filter.

VIII. Conclusions

In this paper, a new APF scheme depends on PSOPI controller, which uses two independent multilayer ANNs with shift method to estimate the fundamental voltage and current components for distribution network is introduced. The feedback signals in this paper are the per-unit values of the voltage and current with an inner inductor voltage feedback loop and an outer current feedback loop for current compensation. The proposed filter is tested and compared with the DPC on a 13 bus industrial distribution system and the results can be summarized in the following points:

- Applying PSOPI controller improves 92.4% of THDv% of the measured cases at all the system buses, while it improves all the average THDv% at system buses with respect to DPC.
- The THDi% of the linear load (bus 29:LL), which is connected in parallel with the harmonic load, with PSOPI controller filter is less than the allowable limit in all the 12 study cases, while without filter it exceeds the limit in 7 cases.
- The THDi% at bus 29 with PSOPI filter do not exceed the 5% allowable limit in all cases, while with DPC filter exceed the limit in five cases (cases #8, #10, #13, #16, and #18).

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